



RAJKIYA ENGINEERING COLLEGE
KANNAUJ, (UP)-209732
(A Government Engineering College)

Ref.127/T.P.O. /R.E.C./Kannauj/2023-24

Date: 11.03.2024

Notice

Cadence is going to have a Campus Recruitment Drive at Cadence Design Systems, Noida campus for Electronics Engineering final year students. It is mandatory for all students to participate for the Cadence Campus Recruitment Drive on 19th March 2024.

S No	Grade/ Title	Job description	Duration	CTC
1	T0-Intern Design Engineer	Follow the Training Programme Designed in View to prepare for Design Engineer Role in 2024.	9 to 10 Months	4.8 lacs
2	T1-Design Engineer	The Work involves creating the Layout Design of Electronics Circuits using Latest CMOS foundry technology process and Tools Like Virtuoso and PVS. You will have to Design Circuits Like PLL, Transmitter, and Receivers working in Multi Giga herts Range Taking care of all Quality checks. Start Working on the Real Projects of Chip Design using the Industry Standard Tools and Academic knowledge sharpened through the training.	NA	16 lacs

Interested candidates from **B.Tech (EC—2024 passing batch)** can register using the link given below latest by 12 march 2024 till 11PM.

Registration Link- <https://forms.gle/UnuL7TRFfvWXd7h57>

In-charge
Training & Placement

Copy to: –

1. Director Ma'am, for her kind Information.
2. Registrar Office.
3. All Departmental coordinator.
4. All Notice Board.

In-charge,
Training & Placement