Ref. 124 /T.P.O. /R.E.C./Kannauj/2023-24   **Date:02/03/2024**

Notice

It is to inform that Training and Placement Cell, REC Kannauj in association with Maven Silicon is conducting a one month e-certification course in VLSI SOC design using Verilog HDL for B.Tech (EE & EL- Second year to Final year) students. It is mandatory for all the eligible students to fill the below-given form for the Maven Silicon by 03 March-2024.

The important details are as follows:

Eligibility: B.Tech in 2nd, 3rd and Final Year of Electronics and Electrical Department.

Course fee – Free

Students can register for the course on this link-

https://docs.google.com/forms/d/e/1FAIpQLSeze8ZQP0E9zNnAbcQ_NYp0-dwNCpLalswIfYuoe-ua04Q7Fg/viewform

Batch starts from 6th March 2024

 Course link: https://elearn.maven-silicon.com/vlsi-soc-design-using-verilog-hdl

In-charge
Training & Placement

Copy to: –
1. Director Ma'am, for her kind Information.
2. Registrar Office.
3. All Notice Board.

In-charge,
Training & Placement